



INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	1
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Complete if Known

Application Number	10727,106
Filing Date	December 3, 2003
First Named Inventor	Kiehl, <i>et al.</i>
Art Unit	2816 2819
Examiner Name	FRD J. Cho
Attorney Docket Number	2003 P 52829 US

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

NON PATENT LITERATURE DOCUMENTS

NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
JL	A	P.R. GRAY, et al. "Analysis and Design of Analog Integrated Circuits" ISBN 0-471-87493-0, 1984 Second Edition, pp. 59-67, John Wiley & Sons, Inc., New York
		No mention

Examiner Signature	<i>James Ch...</i>	Date Considered	6/23/08
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MRCR 500.2. Do not initial if not considered.

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Considered	6/23/08
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